
Subject: Re: Choke capacitance, round 2

Posted by [PakProtector](#) on Tue, 28 Mar 2006 20:06:08 GMT

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Hey-Hey!!!,The choke in question is a CT grid choke. On PS inductor resonance, it is not a bad practice. One LC stage to remove nearly everything but a 120 cps ripple, then a second LC with a tuning C shunted across the L to establish its resonance at 120 cps. Nearly pure DC is the result.The numeric analysis would seem to indicate such a widely varying load on the driver stage would be bad news...these are of course the same methods that would have us using SS for amplification....)cheers,Douglas
